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(11) **EP 1 132 882 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
05.06.2002 Bulletin 2002/23

(51) Int Cl.7: **G09G 3/32**

(43) Date of publication A2:
12.09.2001 Bulletin 2001/37

(21) Application number: **01302037.5**

(22) Date of filing: **06.03.2001**

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR**
Designated Extension States:
AL LT LV MK RO SI

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(30) Priority: **06.03.2000 KR 2000011056**

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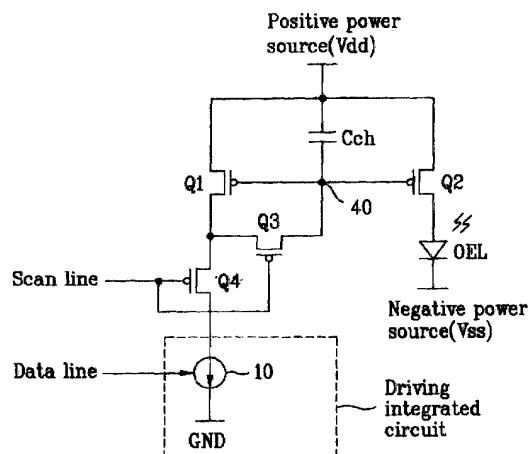
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(54) **Active driving circuit for display panel**

(57) An active driving circuit for a display panel includes first to fourth transistors, a capacitor, a constant current source, and a capacitor. The first transistor is connected with a positive power source. The second transistor has a common gate terminal together with the first transistor and a mirror circuit against the first transistor. Also, the second transistor is turned on by a common gate signal applied to the common gate terminal to supply the positive power source to a display device. The third transistor sets a saturated threshold voltage for the common gate terminal by allowing the first transistor and the second transistor to constitute a mirror circuit against each other in accordance with a scan line signal. The constant current source supplies a current with a ground one side and controlled by a gray signal of a data line. The fourth transistor sets the common gate voltage corresponding to the controlled current of the constant current source by the scan line signal. The capacitor accumulates charges corresponding to the difference between the positive power source and the common gate voltage.

FIG. 2A





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EUROPEAN SEARCH REPORT

Application Number
EP 01 30 2037

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Y	-& US 6 091 203 A 18 July 2000 (2000-07-18) *assumed translation of JP11282419* * column 7, line 35 - column 10, line 30; figures 3-6 *	3	
X	WO 99 65012 A (KONINKL PHILIPS ELECTRONICS NV ;PHILIPS SVENSKA AB (SE)) 16 December 1999 (1999-12-16) * page 7, line 27 - page 14, line 12; figures 1-3 *	1,2,4-10	
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The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 3 April 2002	Examiner Harke, M
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03/02 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
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